

DIM800XSM33-F000

Single Switch IGBT Module

Replaces DS5906-1.2

DS5906-2 August 2011 (LN28652)

FEATURES

- 10µs Short Circuit Withstand
- High Thermal Cycling Capability
- Soft Punch Through Silicon
- Isolated AlSiC Base with AlN Substrates
- Lead Free Construction
- 10.2kV Isolation Package

APPLICATIONS

- High Reliability Inverters
- Motor Controllers
- Traction Drives
- Choppers

The Powerline range of high power modules includes half bridge, chopper, dual, single and bi-directional switch configurations covering voltages from 1200V to 6500V and currents up to 2400A.

The DIM800XSM33-F000 is a single switch 3300V, soft punch through n-channel enhancement mode, insulated gate bipolar transistor (IGBT) module. The IGBT has a wide reverse bias safe operating area (RBSOA) plus 10µs short circuit withstand. This device is optimised for traction drives and other applications requiring high thermal cycling capability.

The module incorporates an electrically isolated base plate and low inductance construction enabling circuit designers to optimise circuit layouts and utilise grounded heat sinks for safety.

ORDERING INFORMATION

Order As:

DIM800XSM33-F000

Note: When ordering, please use the complete part number

KEY PARAMETERS

V_{CES}		3300V
V _{CE(sat)}	* (typ)	2.8V
I _C	(max)	800A
I _{C(PK)}	(max)	1600A

^{*} Measured at the auxiliary terminals

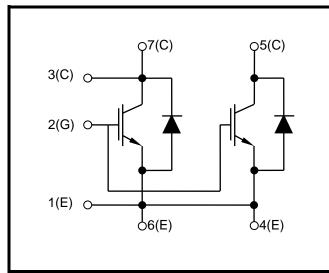


Fig. 1 Circuit configuration

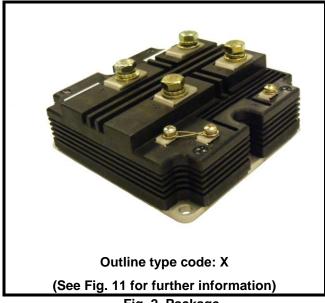


Fig. 2 Package



ABSOLUTE MAXIMUM RATINGS

Stresses above those listed under 'Absolute Maximum Ratings' may cause permanent damage to the device. In extreme conditions, as with all semiconductors, this may include potentially hazardous rupture of the package. Appropriate safety precautions should always be followed. Exposure to Absolute Maximum Ratings may affect device reliability.

T_{case} = 25°C unless stated otherwise

Symbol	Parameter	Test Conditions	Max.	Units
V _{CES}	Collector-emitter voltage	V _{GE} = 0V	3300	V
V_{GES}	Gate-emitter voltage		±20	V
I _C	Continuous collector current	$T_{case} = 90$ °C	800	Α
I _{C(PK)}	Peak collector current	1ms, T _{case} = 115°C	1600	Α
P _{max}	Max. transistor power dissipation	$T_{case} = 25^{\circ}C, T_{j} = 150^{\circ}C$	10400	W
l ² t	Diode I ² t value	$V_R = 0$, $t_p = 10$ ms, $T_j = 125$ °C	320	kA ² s
V _{isol}	Isolation voltage – per module	Commoned terminals to base plate. AC RMS, 1 min, 50Hz	10.2	kV
Q_{PD}	Partial discharge – per module	IEC1287, V ₁ = 6900V, V ₂ = 5100V, 50Hz RMS	10	рС

THERMAL AND MECHANICAL RATINGS

Internal insulation material:

Baseplate material:

Creepage distance:

Clearance:

CTI (Comparative Tracking Index):

AIN

AISiC

56mm

26mm

>600

Symbol	Parameter	Test Conditions	Min	Тур.	Max	Units
R _{th(j-c)}	Thermal resistance – transistor	Continuous dissipation – junction to case		-	12	°C/kW
R _{th(j-c)}	Thermal resistance – diode	Continuous dissipation – junction to case	-	-	24	°C/kW
R _{th(c-h)}	Thermal resistance – case to heatsink	Mounting torque 5Nm (with mounting grease)	-	-	8	°C/kW
Tj	Junction temperature	Transistor	-	-	150	°C
		Diode	-	-	125	°C
T _{stg}	Storage temperature range	age temperature range -		-	125	°C
		Mounting – M6	-	-	5	Nm
	Screw torque	Electrical connections – M4	-	-	2	Nm
		Electrical connections – M8	-	-	10	Nm



ELECTRICAL CHARACTERISTICS

 T_{case} = 25°C unless stated otherwise.

Symbol	Parameter	Test Conditions		Тур	Max	Units
	Collector cut-off current	$V_{GE} = 0V$, $V_{CE} = V_{CES}$			4	mA
I _{CES}		$V_{GE} = 0V$, $V_{CE} = V_{CES}$, $T_{case} = 125$ °C			60	mA
I _{GES}	Gate leakage current	$V_{GE} = \pm 20V, V_{CE} = 0V$			1	μΑ
V _{GE(TH)}	Gate threshold voltage	$I_C = 80$ mA, $V_{GE} = V_{CE}$	5.5	6.5	7.0	V
\	Collector-emitter saturation	V _{GE} = 15V, I _C = 800A		2.8		V
V _{CE(sat)} †	voltage	V _{GE} = 15V, I _C = 800A, T _j = 125°C		3.6		V
I _F	Diode forward current	DC		800		Α
I _{FM}	Diode maximum forward current	t _p = 1ms		1600		Α
\/ t	Diode forward voltage	I _F = 800A		2.9		V
V_F^{\dagger}		I _F = 800A, T _j = 125°C		3.0		V
C _{ies}	Input capacitance	$V_{CE} = 25V, V_{GE} = 0V, f = 1MHz$		144		nF
Q_g	Gate charge	±15V		20		μC
C _{res}	Reverse transfer capacitance	$V_{CE} = 25V, V_{GE} = 0V, f = 1MHz$		2.2		nF
L _M	Module inductance			15		nΗ
R _{INT}	Internal resistance			135		μΩ
SC _{Data}	Short circuit current, I _{SC}	$T_{j} = 125$ °C, $V_{CC} = 2500V$ $t_{p} \le 10 \mu s$, $V_{GE} \le 15 V$ $V_{CE (max)} = V_{CES} - L^{*} x dI/dt$ IEC 60747-9		3700		А

Note:

 $^{^{\}dagger}$ Measured at the auxiliary terminals * L is the circuit inductance + $L_{\rm M}$



ELECTRICAL CHARACTERISTICS

T_{case} = 25°C unless stated otherwise

Symbol	Parameter	Test Conditions		Min	Тур.	Max	Units
$t_{d(off)}$	Turn-off delay time		$R_{G(ON)} = 3.9\Omega$ $R_{G(OFF)} = 6.2\Omega$		3.0		μs
t _f	Fall time	$I_{C} = 800A$ $V_{GE} = \pm 15V$ $V_{CE} = 1800V$ $C_{ge} = 220nF$ $L_{S} \sim 100nH$			270		ns
E _{OFF}	Turn-off energy loss				1050		mJ
t _{d(on)}	Turn-on delay time				1300		ns
t _r	Rise time				275		ns
E _{ON}	Turn-on energy loss		$R_{G(ON)} = 2.7\Omega$ $R_{G(OFF)} = 6.2\Omega$		1250		mJ
Q _{rr}	Diode reverse recovery charge	I _F = 800A			320		μC
I _{rr}	Diode reverse recovery current	$V_{CE} = 1800V$ $dI_F/dt = 4000A/\mu s$		670		Α	
E _{rec}	Diode reverse recovery energy		ŀ000A/μs		300		mJ

T_{case} = 125°C unless stated otherwise

Symbol	Parameter	Test Conditions		Min	Тур.	Max	Units
t _{d(off)}	Turn-off delay time		$R_{G(ON)} = 3.9\Omega$ $R_{G(OFF)} = 6.2\Omega$		3.1		μs
t _f	Fall time	$I_{C} = 800A$ $V_{GE} = \pm 15V$ $V_{CE} = 1800V$ $C_{ge} = 220nF$ $L_{S} \sim 100nH$			280		ns
E _{OFF}	Turn-off energy loss				1200		mJ
t _{d(on)}	Turn-on delay time				1200		ns
t _r	Rise time				315		ns
E _{ON}	Turn-on energy loss		$R_{G(ON)} = 2.7\Omega$ $R_{G(OFF)} = 6.2\Omega$		1750		mJ
Q_{rr}	Diode reverse recovery charge	$I_F = 800A$ $V_{CE} = 1800V$			600		μC
I _{rr}	Diode reverse recovery current				800		Α
E _{rec}	Diode reverse recovery energy	dl _F /dt = 4	4000A/μs		600		mJ



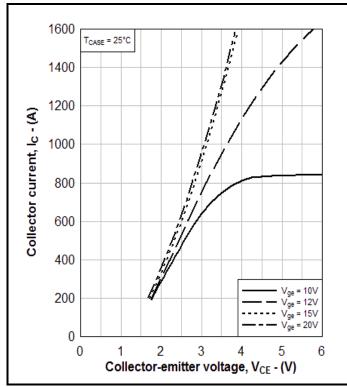


Fig. 3 Typical output characteristics

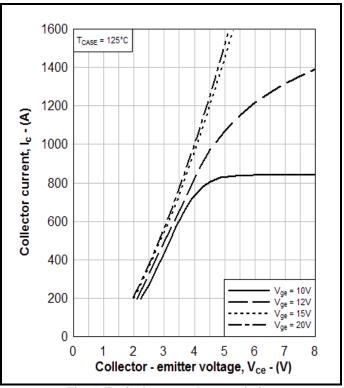


Fig. 4 Typical output characteristics

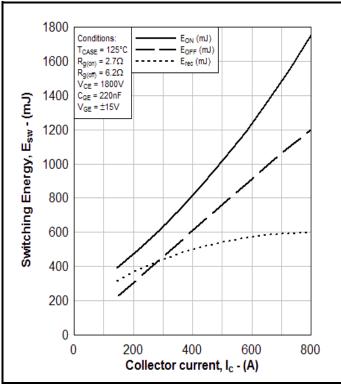


Fig. 5 Typical switching energy vs collector current

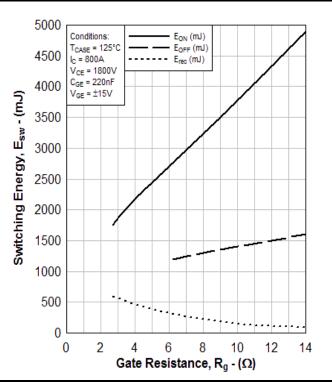


Fig. 6 Typical switching energy vs gate resistance



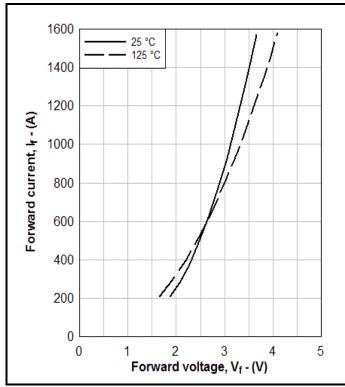


Fig. 7 Diode typical forward characteristics

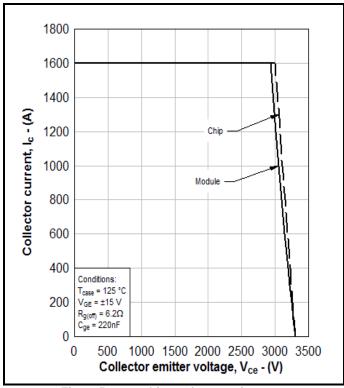


Fig. 8 Reverse bias safe operating area

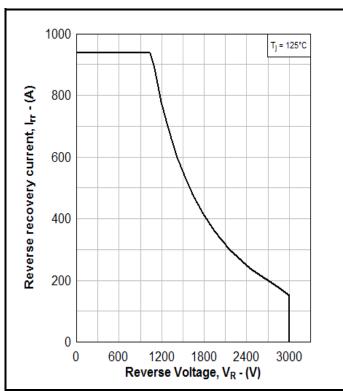


Fig. 9 Diode reverse bias safe operating area

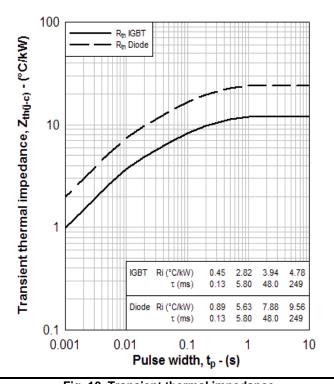


Fig. 10 Transient thermal impedance



PACKAGE DETAILS

For further package information, please visit our website or contact Customer Services. All dimensions in mm, unless stated otherwise.

DO NOT SCALE.

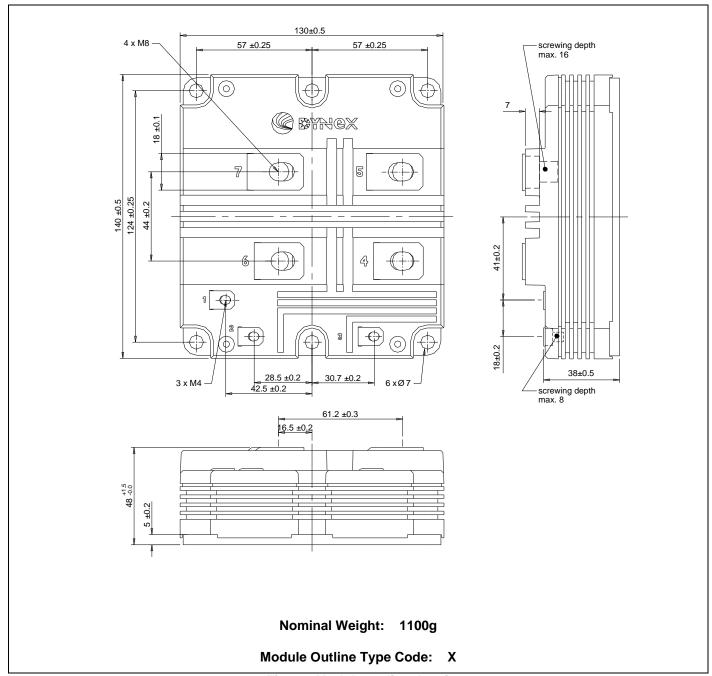


Fig. 11 Module outline drawing



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